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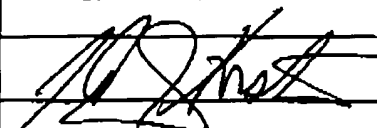
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<b>TRANSMITTAL FORM</b>  <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/954,861
	Filing Date	09/18/2001
	First Named Inventor	Miller
	Group Art Unit	2818
	Examiner Name	Hoang, Quoc Dinh
Total Number of Pages in This Submission		8
		Attorney Docket Number 1039.co2

ENCLOSURES (check all that apply)		
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re: Miller, et al.

Serial No: 09/954,861

Filed: 09/18/2001

For: Process for Integrating Dielectric  
Optical Coatings into Micro-  
Electromechanical Devices

Patent Application

Group: 2816

Examiner: Unknown

AMENDMENT UNDER RULE 111Assistant Commission for Patents  
Washington, D.C. 20231

Sir:

In response to the pending Office Action, mailed July 3, 2002 (Paper No. 6), Applicants hereby amend the above-captioned application and request reconsideration in view of the following remarks.

**In the Claims:***Please amend claim 1, as follows:*

1. (amended) A process for fabricating a deflectable optical MEMS structure having a dielectric coating, the process comprising:
- forming a device layer;
  - depositing a multilayer reflective dielectric optical coating over the device layer;
  - depositing a mask layer over the device layer;
  - patterning the mask layer;
  - transferring a pattern of the mask layer into the dielectric coating; and
  - removing at least part of a sacrificial layer to release the device layer.

*Please add new claims 16 and 17, as follows:*